

Yang Yang

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EDUCATION

- **The Australian National University (ANU)** Canberra, Australia
Doctor of Philosophy, Engineering and Computer Science, supervised by Prof. Liang Zheng May 2022 - Present
- **Huazhong University of Science and Technology (HUST)** Wuhan, China
Bachelors of Electronic Information Engineering *GPA: 3.7 / 4.0* Sep 2017 - Jun 2021

RESEARCH INTERESTS

- **Efficient and Scalable (Multimodal) LLMs:** Focusing on Recurrent Architectures, Parallel Scaling Laws.

PUBLICATIONS

- **ParVL: Parallel Scaling and Expandable Compute Allocation for Multimodal LLMs** 
Yang Yang, Qinyu Zhao, Lixin Gu, Mouxiang Chen, et al. To be released
- **LinearSR: Unlocking Linear Attention for Stable and Efficient Image Super-Resolution**
Xiaohui Li, Shaobin Zhuang, Shuo Cao, Yang Yang, Yuandong Pu, et al. ICLR 2026
- **Evaluating and Advancing Multimodal Large Language Models in Ability Lens** 
Feng Chen, Chenhui Gou, Jing Liu, Yang Yang, Zhaoyang Li, Jiyuan Zhang, et al. CVPR 2026 (Findings)
- **Bounding Box Stability against Feature Dropout Reflects Detector Generalization across Environments** 
Yang Yang, Wenhai Wang, Zhe Chen, Jifeng Dai, Liang Zheng. ICLR 2024 (Spotlight)
- **CIFAR-10-Warehouse: Broad and More Realistic Testbeds in Model Generalization Analysis** 
Xiaoxiao Sun, Xingjian Leng*, Zijian Wang*, Yang Yang*, Zi Huang*, Liang Zheng.* ICLR 2024 (Poster)
- **InternChat: Solving vision-centric tasks by interacting with chatbots beyond language** 
Internchat Team, Shanghai AI Laboratory. Technical Report 2023
- **Does Keypoint Estimation Benefit Object Detection? Empirical Study of One and Two-stage Detectors** 
Yang Yang, Akshay Asthana, and Liang Zheng. FG 2021 (Oral)

EXPERIENCES

- **Shanghai AI Laboratory** Shanghai, China
Research Intern, mentored by Dr. Wenwei Zhang Jun 2025 - Present
 - **LLM Reasoning (Loop Transformer):** Explored loop transformer architectures to enhance mathematical reasoning during SFT; engineered customized KV Cache optimizations for recurrent states and evaluated the efficiency-performance trade-offs between **Implicit and Explicit CoT** (In-progress).
 - **Multimodal Parallel Scaling (ParVL):** Developed ParVL, a parallel scaling framework for MLLMs that breaks rigid computation allocation between ViT and LLM; utilized prefix-tuning and weight reuse to expand computational capacity without increasing total parameters (To be released).
- **TikTok, Data Trust and Safety Group** Sydney, Australia
Research Intern, mentored by Jiyuan Zhang Jul 2024 - Feb 2025
 - Designed *Video-Ruler*, the first benchmark for long-text and short-video understanding; developed a Video Assistant Referee system for automated basketball violation detection based on complex rulebooks.
- **Shanghai AI Laboratory** Shanghai, China
Research Intern, mentored by Dr. Wenhai Wang Feb 2022 - Nov 2023
- **Urban Computing Group of Sensetime Co., LTD.** Beijing, China
Industry Intern, mentored by Bo Li Apr 2021 - Feb 2022
- **The Australian National University** Remotely
Research Assistant, supervised by Prof. Liang Zheng Sep 2020- Apr 2021
- **Chinese University of Hong Kong & SmartMore Co., LTD.** Shenzhen, China
Industry Intern, supervised by Dr. Shu Liu and Prof. Jiaya Jia Jun 2020 - Sep 2020
- **Media and Communications Laboratory, HUST** Wuhan, China
Research Assistant, supervised by Prof. Xinggang Wang Jun 2019 - Sep 2019

ACADEMIC SERVICE

- **Reviewer for conferences:** ICML 2026, ICLR 2026, NeurIPS 2026, ICCV 2025, CVPR 2024, *et al.*
- **Organizer for workshops and challenges:** The 2nd and 3rd CVPR Workshop on Vision Datasets Understanding, The 1st and 2nd DataCV Challenge @ CVPR [↗](#)

SELECTED AWARDS

- 2022 - 2026 ANU Postgraduate Research Scholarship & HDR Fee Remission Merit Scholarship

PROGRAMMING SKILLS

- **Programming Languages:** Python, C, Java, L^AT_EX, Unix Shell Scripts
- **Machine Learning Libraries:** PyTorch, Tensorflow, Mxnet